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Applicant(s): Kwan-Ju Koh) I hereby certify that this paper is
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For: "Semiconductor Device and Method) Box 1450, Alexandria, VA 22313-
of Fabricating the Same") 1450 on this date:
Group Art Unit: 2811) March 15, 2004
Examiner: Unknown	Mal G. Flace
	Mark G. Hanley
	Registration No. 44,736
) Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

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GROSSMAN & FLIGHT, LLC. 20 N. Wacker Drive Suite 4220 Chicago, Illinois 60606 (312) 580-1020

By:

Mark G. Hanley

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March 15, 2004

Form PTO-1449 (Modified) INFORMATION D URE STATEMENT

(Use several sheets if necessary)

U.S. Department of Commerce Patent and Trademark Office

Atty. Docket No. 20067/OPP030893 Serial No.

10/721,659

Applicant

Kwan-Ju Koh

Filing Date

Group Art Unit

November 25, 2003 2811

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01	6,268,276	7/31/2001	Chan et al.			
	A02	6,358,845	3/19/2002	Lou			
	A03	6,376,330	4/23/2002	Fulford, Jr. et al.			
	A04	6,403,461	6/11/2002	Tae et al.			
	A05	6,423,630	7/23/2002	Catabay et al.			
	A06	6,472,719	10/29/2002	Lin et al.			_
	A07						
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	A22						

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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.